

<p>Name : Dr. S. PARVATHI</p> <p>E mail parvathi.ai@sairam.edu.in</p> <p>Employee ID SECT23AI03</p> <p>AU FACULTY CODE: 4125548</p> <p>AICTE ID: 1-43443870075</p>			
<p>Designation:</p>	<p>Associate Professor (Artificial Intelligence & Data Science)</p>		
<p>Area of Specialization :</p>	<p>Multi-core Architectures, Network-on-chip, VLSI Design</p>		
<p>Experience</p>	<p>Teaching: 4 years. Research: 5.6 years Industrial Experience: 02</p>		
<p>No of Workshop/ FDP attended:</p>	<p>No of Workshop</p>		<p>No of FDP</p>
	<p>08</p>		<p>15</p>
<p>Publications:</p>	<p>Conference</p>		<p>Journal</p>
	<p>National</p>	<p>International 02</p>	<p>National International 02(Scopus) 02 (SCI)</p>
<p>Patents</p>	<p>National</p>		<p>International</p>
			<p>01</p>
<p>Professional body membership</p>	<p>IEEE</p>		
<p>Awards and Achievements</p>	<p>Secured first placed in PG</p>		

SCI PUBLICATIONS

1. Discrete Firefly Algorithm for Optimizing Topology Generation and Core Mapping of Network-on-Chip, JOURNAL OF INTELLIGENT AND SOFT COMPUTING, vol. 34, no.1, pp. 15 – 32, jan 2022.
2. Load Based Dynamic Priority arbiter for NoC Architecture. JOURNAL OF SCIENTIFIC AND INDUSTRIAL RESEARCH, vol. 81, no. 5, pp.495 – 504, April 2022

CONFERENCE PAPERS

1. Improved Network Architecture with Efficient Resource Allocation in Vehicular Communication.
2. ENCS: A Novel Approach for Identifying Pneumonia using Chest Radiographs Based on Enhanced Neural Classification Scheme.
3. Energy-conscious Edge-Assisted IoT Communication Framework Leveraging 5G and AI Technologies.
4. Power Management In Regular Mesh NoC Using DVFS
5. DVFS based Power Mitigation in Regular Mesh NoC.
6. A Survey on Energy and Power Management Techniques In Multi Core Architecture

PATENT TITLE

IOT-BASED NOVEL ONE TOUCH WEARABLE SMART BRACELET FOR STRESS PREDICTION, MAY 2022

LIST OF SEMINAR / SHORT TERM COURSES / FDP/WORKSHOP ATTENDED / ORGANIZED

FDPs

1. *Design Thinking for IDEA Lab* - acted as coordinator
2. Faculty Development Program on *Internet of Things (IoT)* - acted as coordinator, June 2023
3. Faculty Development Program on Large Language Models (*LLM*) - acted as coordinator, January 2025
4. 6 days online FDP on “*Emerging Trends in Artificial Intelligence (AI): Innovations and Applications*”
5. 5 days offline Faculty Industrial Training and Internship on “*Robotics & Automation*”
6. 5 days offline Faculty Development Program on “*Large Language Models*” – acted as coordinator
7. 6 days online FDP on “*IoT in focus: Emerging Trends and Real World Challenges*”
8. 6 days online AICTE – ATAL FDP on “*The Future of Smart Mobility: Integrating AI & IoT in Electric Vehicle Automation*”
9. 6 days online AICTE – ATAL FDP on “*Smart Communication in IoT: Security & Future Applications and Possibilities*”
10. 6 days online AICTE – ATAL FDP on “*Empowering Healthcare with AI and ML: Advances and Applications*”
10. 6 days online AICTE – ATAL FDP on “*Smart Systems & Emerging Technologies in Industry 4.0*”
11. 6 days online AICTE – ATAL FDP on “*Shaping the future of Healthcare with AI, Imaging, Devices & Diagnostics*”
12. 6 days online AICTE – ATAL FDP on “*Next-Gen Aerial Communication: Integrating AI and Drone Technologies*”

13. 6 days online AICTE – ATAL FDP on “**Next-Gen VLSI and Semiconductor Systems: Integrating Machine Learning for Intelligent Design Automation**”

14. 6 days online AICTE – ATAL FDP on “**Robotics for Industry 4.0: Enabling Smart Manufacturing**”

15. 3 days offline FDP on “**Universal Human Values 1**”, Dec 1-3, 2025 at Sri Sairam Engineering College

WORKSHOPS

1. Three Days Workshop on "**Advanced Digital Design Flow using Cadence**", at MIT Campus, Anna University

2. Two Days Workshop on "**System Design using Xilinx Vivado Design Suite and Zynq - 7000**", at Anna University

3. Five Day Summer School Training Programme on "**Artificial Intelligence with Deep Learning and Machine Learning**", at Hindustan University.

4. 3-Day UPE workshop on "**FPGA based system design using NI LabVIEW and CRIO**", under the aegis of the UGC-UPE project, Anna University.

5. Attended a seminar on "**Fulbright Fellowships - Mentoring Workshop**" at IIT Madras

6. Attended a seminar on "**Training Programme on URKUND: Plagiarism detection service**" at TAG Auditorium, CEG Campus, Anna University, Chennai

7. Two Days Workshop on “**Hands-on Training on ANN using MATLAB**”, at VIT Chennai campus.

8. National level Workshop on “**Virtual and Augmented Reality**”

OTHERS

- 1. Completed 10 months certification score from IIT Kharagpur on: “*AI & ML, Data Science and IoT*”**
- 2. NPTEL Believer in Jan- June 2025**
- 3. NPTEL Believer and Discipline star in July-Dec 2025**
- 4. Completed 8 courses in NPTEL**

EDUCATIONAL QUALIFICATION

- 1. Ph.D. (Full time) 2017 – 2022, Viva-Voce – DEC 15, 2022. ANNA UNIVERSITY, MIT CAMPUS, Degree under ICE (Information and Communication Engineering)**
- 2. M.E. – 2013 – 2015, VLSI DESIGN, ANNA UNIVERSITY - 8.01 CGPA**
- 3. B.E. – 2000 – 2004, ECE, UNIVERSITY OF MADRAS - 78% - First Class with Distinction**

Declaration: I hereby declare that all the information mentioned above is true to my knowledge and I have all supporting documents for the facts mentioned above.

DR. S. PARVATHI