


Name : DR. S. PARVATHI E mail parvathi.ai@sairam.edu.in Employee ID ET23AI03			
Designation:	Associate Professor (Artificial Intelligence & Data Science)		
Area of Specialization :	Multi-core Architectures, Network-on-chip, VLSI Design		
Experience	Teaching: 4 years. Research: 5.6 years Industrial Experience: 02		
No of Workshop/ FDP attended:	No of Workshop		No of FDP
	08		15
Publications:	Conference		Journal
	National	International 02	National International 01(Scopus) 02 (SCI)
Patents	National		International
			01
Professional body membership	IEEE		
Awards and Achievements	Secured first placed in PG		
Publications	1. LOAD BASED DYNAMIC PRIORITY ARBITER FOR NOC ARCHITECTURE, JOURNAL OF SCIENTIFIC AND INDUSTRIAL RESEARCH, vol. 81, no. 5, pp.495 – 504, april 2022. 1. Journal (SCI) 2. DISCRETE FIREFLY ALGORITHM FOR OPTIMIZING TOPOLOGY GENERATION AND CORE MAPPING OF NETWORK-ON-CHIP, JOURNAL OF INTELLIGENT AND SOFT COMPUTING, vol. 34, no.1, pp. 15 – 32, jan 2022.		

2. Journal (Scopus)	1, IMPROVED NETWORK ARCHITECTURE WITH EFFICIENT RESOURCE ALLOCATION IN VEHICULAR COMMUNICATION, ICFTS,2025,PP.1-6, DOI: 10.11/09.ICFTS62006.2035.11031547
3.Conference (International)	1. POWER MANAGEMENT IN REGULAR MESH NOC USING DVFS, ICRTECC 2019. 2. DVFS BASED POWER MITIGATION IN REGULAR MESH NOC INDIANA SUMMIT 2019. 3. A SURVEY ON ENERGY AND POWER MANAGEMENT TECHNIQUES IN MULTI CORE ARCHITECTURE, 4 th IEEE International Symposium on Smart Electronic Systems
4.Patent	PATENT TITLE: IOT-BASED NOVEL ONE TOUCH WEARABLE SMART BRACELET FOR STRESS PREDICTION, MAY 2022

LIST OF SEMINAR/ SHORT TERM COURSES/FDP/WORKSHOP ATTENDED

FDPs

1. ***Design Thinking for IDEA Lab*** - acted as coordinator
2. Faculty Development Program on ***Internet of Things (IoT)*** - acted as coordinator
3. 6 days online FDP on ***“Emerging Trends in Artificial Intelligence (AI): Innovations and Applications”***
4. 5 days offline Faculty Industrial Training and Internship on ***“Robotics & Automation”***
5. 5 days offline Faculty Development Program on ***“Large Language Models”*** – acted as coordinator
6. 6 days online FDP on ***“IoT in focus: Emerging Trends and Real World Challenges”***
7. 6 days online AICTE – ATAL FDP on ***“The Future of Smart Mobility: Integrating AI & IoT in Electric Vehicle Automation”***
8. 6 days online AICTE – ATAL FDP on ***“ Smart Communication in IoT: Security & Future Applications and Possibilities”***
9. 6 days online AICTE – ATAL FDP on ***“Empowering Healthcare with AI and ML: Advances and Applications”***
10. 6 days online AICTE – ATAL FDP on ***“Smart Systems & Emerging Technologies in Industry 4.0”***

11. 6 days online AICTE – ATAL FDP on “Shaping the future of Healthcare with AI, Imaging, Devices & Diagnostics”

12. 6 days online AICTE – ATAL FDP on “Next-Gen Aerial Communication: Integrating AI and Drone Technologies ”

13. 6 days online AICTE – ATAL FDP on “Next-Gen VLSI and Semiconductor Systems: Integrating Machine Learning for Intelligent Design Automation ”

14. 6 days online AICTE – ATAL FDP on “Robotics for Industry 4.0: Enabling Smart Manufacturing”

15. 3 days offline FDP on “Universal Human Values 1”

WORKSHOPS

1. Three Days Workshop on "*Advanced Digital Design Flow using Cadence*", at MIT Campus, Anna University

2. Two Days Workshop on "*System Design using Xilinx Vivado Design Suite and Zynq - 7000*", at Anna University

3. Five Day Summer School Training Programme on "*Artificial Intelligence with Deep Learning and Machine Learning*", at Hindustan University.

4. 3-Day UPE workshop on "*FPGA based system design using NI LabVIEW and CRIO*", under the aegis of the UGC-UPE project, Anna University.

5. Attended a seminar on "*Fulbright Fellowships - Mentoring Workshop*" at IIT Madras

6. Attended a seminar on "*Training Programme on URKUND: Plagiarism detection service*" at TAG Auditorium, CEG Campus, Anna University, Chennai

7. Two Days Workshop on “*Hands-on Training on ANN using MATLAB*”, at VIT Chennai campus.

8. National level Workshop on “*Virtual and Augmented Reality*”

OTHERS

1. Completed 10 months certification score from IIT Kharagpur on: “*AI & ML, Data Science and IoT*”

2. NPTEL Believer in Jan- June 2025

3. NPTEL Believer and Discipline star in July-Dec 2025

EDUCATIONAL QUALIFICATION

1. **Ph.D. (Full time)** 2017 – 2022, Viva-Voce – DEC 15, 2022. ANNA UNIVERSITY, MIT CAMPUS, Degree under ICE (Information and Communication Engineering)

2. **M.E.** – 2013 – 2015, **VLSI DESIGN**, ANNA UNIVERSITY – 8.01 - CGPA

3. **B.E.** – 2000 – 2004, **ECE**, UNIVERSITY OF MADRAS - 78% - First Class with Distinction

Declaration: I hereby declare that all the information mentioned above is true to my knowledge and I have all supporting documents for the facts mentioned above.

DR. S. PARVATHI